

Schedule

Time	Monday July 20	Tuesday July 21	Wednesday July 22	Thursday July 23	Friday July 24
7:30	[7:30] Continental Breakfast, Hilton Québec Level 1, Porte du Palais	[7:30] Continental Breakfast, Hilton Québec Level 1, Foyer/Anteroom	[7:30] Continental Breakfast, Hilton Québec Level 1, Foyer/Anteroom	[7:30] Continental Breakfast, Hilton Québec Level 1, Foyer/Anteroom	[7:30] Continental Breakfast, Hilton Québec Level 1, Foyer/Anteroom
8:00	[8:00] Short Course Introduction Ken LaBel Multipurpose Hall 2000A/B				
8:30	[8:10] Part 1 – Programmatic Aspects Overview John M. Stone	[8:30] Conference Opening Multipurpose Hall 2000A/B	[8:30] Invited Talk Québec: World Heritage City Barry Lane Multipurpose Hall 2000A/B		[8:30] Invited Talk The Road that Walks, A History of Travel on the St. Lawrence River David Mendel Multipurpose Hall 2000A/B
9:00		[9:10] Session A Single-Event Effects: Mechanisms and Modeling		[9:00] Invited Talk Understanding Hardness- by-Design Approaches for Fabricating Rad-Hard Microelectronic Components Ronald C. Lacoé Multipurpose Hall 2000A/B	
9:30	[9:20] Part 2 – Radiation Effects Point of View Keith Avery		[9:40] Session D Photonic Devices and Integrated Circuits	[9:30] Session G Hardening by Design	[9:40] Session I Single-Event Effects: Devices and Integrated Circuits
10:00					
10:30	[10:30] Break, Foyer 2000	[10:15] Break, MultiPurpose Hall 2000C/D	[10:30] Break, MultiPurpose Hall 2000C/D	[10:35] Break, Foyer 2000	[10:30] Break, Foyer 2000
11:00	[10:55] Part 3 – Reliability Considerations Doug Sheldon	[10:45] Session A (continued)	[11:00] Session E Hardness Assurance	[11:05] Session H Space and Terrestrial Environments	[11:00] Session I (continued)
11:30					
12:00	[12:00] Short Course Luncheon, Porte du Palais/Kent/St. Louis Rooms, Québec Hilton Level 1	[12:15] Lunch	[12:05] Lunch	[11:55] Lunch	
12:30					[12:30] End of Conference
1:00					
1:30	[1:10] Part 4 – Electronic Designer's Perspective Dr. Kirk Kohnen and Kay Jobe	[1:45] Session B Basic Mechanisms of Radiation Effects	[1:30] Session F Dosimetry	[1:30] Invited Talk Combating Cyber Crime: A New Canadian Cyber Forensic Organization Dr. Mourad Debbabi	
2:00					
2:30	[2:15] Part 5 – Example 1: Trading ASIC and FPGA Considerations for System Insertion Melanie Berg		[2:35] Poster Session Porte St-Louis, Porte Kent and Porte du Palais Rooms, Québec Hilton Level 1	[2:30] Data Workshop Porte St-Louis, Porte Kent and Porte du Palais Rooms, Québec Hilton Level 1	
3:00	[3:15] Break, Foyer 2000	[3:05] Break, MultiPurpose Hall 2000C/D			
3:30					
4:00	[3:40] Part 6 – Example 2: Trading Mixed Signal Devices for Instrument or High-Precision Applications Dr. Steven C. Moss	[3:35] Session C Radiation Effects in Devices and Integrated Circuits			
4:30	[4:40] Wrap-up				
5:00	[4:50] Exam (for students requesting CEU credit only)	[4:55] End of Sessions	[5:00] End of Sessions	[4:45] End of Sessions	
5:30	[5:20] End of Short Course			[5:15 to 7:00] Radiation Effects Committee Open Meeting, MultiPurpose Hall 2000A/B	
6:00		[6:00 to 10:00] Industrial Exhibits Reception 6:00 Cocktails 7:00 Buffet MultiPurpose Hall 2000C/D			
6:30			[6:30 to 11:30] Conference Social Evening Dinner Cruise 8:15 Dinner		